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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yi Ding
Assignee: ProMOS Technologies, Inc.
Title: Nonvolatile Memory Cells With Buried Channel Transistors
Application No.: 10/632,155 Filing Date: July 30, 2003
Examiner: Nhu, David Group Art Unit: 2818
Docket No.: M-15222 US

San Jose, California
May 31, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(c)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed except for United States Patents and United States Published Patent Applications.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
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Michael Shenker 5-31-05

Michael Shenker Attorney for Applicant Date of Signature

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.	Serial No.		
				M-15222 US	10/632,155		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT JUN 02 2005 PATENT & TRADEMARK OFFICE 61				Applicant			
(Use several sheets if necessary)				Yi Ding			
				Filing Date	Group		
				July 30, 2003	2818		
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,445,983	Aug. 1995	Hong			
	AB	5,910,912	Jun. 1999	Hsu et al.			
	AC	5,918,124	Jun. 1999	Sung			
	AD	6,040,216	Mar. 2000	Sung			
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	AL	6,635,533	Oct. 2003	Chang et al.			
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	AS						
	AT						
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Examiner		Date Considered					
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